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<u>REMARKS</u>

Present Status of the Application

The Office Action rejected all presently-pending claims 1-5 and 10-14. Specifically, the

Office Action rejected claims 1, 3-5, 10-12 and 14 35 U.S.C. 102, as being anticipated by

Castleman (U. S. Patent 5,570,002). In addition, the Office action also rejected claims 7 and 13

as being unpatentable over Castleman in view of Batson et al. (U. S. Patent 6,400,043). Claims

1- 5 and 10-14 remain pending in the present application, and reconsideration of those claims is

respectfully requested.

Summary of Applicant's Invention

The Applicant's invention is directed to a universal power supply system. The universal

power supply system can supply power to the different devices without restriction due to, for

example, power pattern or outer appearance of connector.

Discussion of Office Action Rejections

The Office Action rejected all presently-pending claims 1-5 and 10-14. Specifically, the

Office Action rejected claims 1, 3-5, 10-12 and 14 35 U.S.C. 102, as being anticipated by

Castleman. The Office action also rejected claims 7 and 13 as being unpatentable over

Castleman in view of Batson et al.. Applicants respectfully traverse the rejections for at least the

reasons set forth below.

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In the previous response filed on March 19, 2003, Applicants argues that claims 1, 10 and 11 respectively recite:

1. A universal power supply (UPS) system, comprising:

a device circuit, wherein the device circuit comprises

a device ID code unit, storing a device ID (identification) code of the device circuit, and

a power input interface, used to transmit a needed power to the device circuit;

a UPS unit, wherein the UPS unit comprises

a voltage supply unit,

a device ID detecting unit that can detect the device ID code stored in the device ID code unit of the device circuit, and

a voltage control unit, that controls the voltage unit to supply a power type of the needed power, according to the device ID code; and

a standard interface unit, coupled between the device circuit and the UPS unit, to supply the needed power to the device circuit and transmitting the device ID code.

10. A universal power supply (UPS) system, comprising:

a device circuit;

a UPS unit; and

circuit:

a standard interface unit, coupled between the device circuit and the UPS unit, thereby to supply a needed power to the device circuit.

11. A method for supplying power to a device circuit, comprising:

providing a device circuit having a power input interface and a battery;

pre-setting a device ID code to the device circuit;

detecting the device ID code, to recognize a power type needed by the device

providing a power source apparatus; and

controlling the power source apparatus, according to the power type, to supply a power to the device circuit through the power input interface, wherein the device circuit can also receive a power from the battery through the power input interface.

The features emphasized above are at least not disclosed by the prior art references.

As argued in previous response, the *power input interface* 118 in FIG. 2 is used to, for example, receive the power form the batter 114 or UPS unit 100 (page 5). The *standard*

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interface unit is used to adapt various supplied devices, which have different power patterns or the connector outer appearance. The supplied device may need at least two power lines with the specific power pattern. For example, the device with two power lines and the device with three power lines can be connected to the same standard interface unit without worrying about the power pattern and the connector appearance. Due to the standard interface unit, the connector appearance is the same. It is not necessary, like the current various types of connector in the market, to worry about the match between the plug and the receptacle due to different appearance and power pattern. For example, one type of cellular phone needs a specific battery (page 1).

The Office Action in "Response to Arguments" refers to Castleman at Fig. 1 (col. 10, lines 45+) about the power input interface and refers to Fig. 2 (col. 11, lines 28+) about the standard input interface. Applicants respectfully disagree.

In re Castleman, in Fig. 1, the connector 38 with the terminal 39 does not teach the switch function for example between the batter power or the UPS. Also and, the power input interface 118 is implemented in the supplied device so as to receive the power to export to the device circuit. The supplied device in Castleman only has the terminal 41 to connect to the terminal 39 of the connector 38. The units 20 and 31 also failed to disclose the power input interface.

In addition about the standard interface unit, in Fig. 2, clearly the supplied devices 140a-140y has two-wire connector. However, the supplied device 140z has three-wire connector (col. 12, lines 57-65). Therefore, a specific type of supplied device should be connected to a specific

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path 126's by a specific connector (two-wire or three-wire). However, this is not equivalent to features of the present invention. The present invention only needs the standard interface unit associating with the UPS, which interface unit can adapt various types of supplied devices without limited to the power pattern and the connector specification.

For example, the supplied device 140a is not expected to be plugged into the paths 126r, 126y, and particularly in 126z via the connectors 138a, ..., 138z. This is different mechanism from the present invention.

Also and, the features discussed above have been stated in the previous response and do not raise new issue.

For at least the foregoing reasons, Applicant respectfully submits that independent claims 1, 10 and 11 patent define over the prior art references, and should be allowed. For at least the same reasons, dependent claims 2-5 and 12-14 patently define over the prior art as well.

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CONCLUSION

For at least the foregoing reasons, it is believed that all pending claims 1-5 and 10-14 are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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